

## EUROPEAN GOVERNMENT AGENCY AT THE FOREFRONT OF CYBERSECURITY SELECTS NAPATECH TO STRENGTHEN NETWORK DEFENSE

COPENHAGEN, Denmark, May 15, 2018 – Napatech™ (OSLO: NAPA.OL), the leading provider of reconfigurable computing platforms, today announced that a European government agency responsible for managing the security of all networks and communications between their nation's departments and other agencies has selected Napatech to strengthen their cyber defense and improve incident response times. The agency needed a cost-effective and scalable solution for real-time visibility over all communications on high-bandwidth network links in order to correlate the events and alarms occurring in sensors and probes, with lossless recordings of the network traffic that triggered the events.

Napatech's reconfigurable computing solution met the agency's needs by providing FPGA-based SmartNIC software and hardware in a low-cost, standard server platform. Combined with Napatech's Pandion capture and storage software solution, the agency was able to meet several critical requirements that were unmet by alternative solutions.

Napatech has received the first purchase order from this customer.

## Jarrod Siket, chief marketing officer, Napatech, said:

"Napatech's reconfigurable computing platform is powered by FPGA SmartNIC software and hardware, and supports a wide-rage of networking and security applications. We are proud to see the Pandion solution for network recording playing such a pivotal role in cybersecurity and protecting nations in an increasingly volatile landscape."

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## **ABOUT NAPATECH**

Napatech helps companies to reimagine their business by bringing hyperscale computing benefits to IT organizations of every size. We enhance open and standard virtualized servers to boost innovation and release valuable computing resources that improve services and increase revenue. Our Reconfigurable Computing Platform<sup>TM</sup> is based on a broad set of FPGA software for leading IT compute, network and security applications that are supported on a wide array of FPGA hardware designs.

Additional information is available at www.napatech.com