

NAPATECH SHOWCASES RECONFIGURABLE COMPUTING SOLUTIONS FOR 5G MOBILE NETWORK OPERATORS AT INTEROP TOKYO

COPENHAGEN, Denmark, June 12, 2018 – Napatech™ (OSLO: NAPA.OL), the leading provider of reconfigurable computing platforms, today announced it will showcase its Reconfigurable Accelerated RAN solution for 5G mobile network operators at Interop Tokyo from June 13 to 15. The Napatech Reconfigurable Computing Platform is based on highly-programmable FPGA-based SmartNIC software and hardware that radically improves the performance of network and security applications deployed on standard server platforms in mobile networks.

Napatech's industry expert on reconfigurable computing, Kazuo Nozawa, will be speaking at the conference on June 15, at 16:00 at location 4Y20. Additionally, during the show, the Napatech solution will be on display at booth 5C20.

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.@Napatech Showcases Reconfigurable Accelerated RAN for #5G #Mobile Network Operators @InteropTokyo30: <https://tinyurl.com/y89nrsrp> #FPGA

Napatech's reconfigurable computing solution for mobile network operators helps them fulfill their vision for a fully software-defined network, without compromise, by providing:

- **Hyperscale economics:** The solution enables operators to deploy their 5G network, security and analytics applications as software-only solutions on open, standard, high-volume, low-cost server platforms, enhanced with FPGA-based SmartNIC software and hardware.
- **Hyperscale performance:** The FPGA-based SmartNIC software and hardware deliver line rate networking performance for all traffic, across 1/10/25/40/50/100 gigabit Ethernet interfaces, while simultaneously offloading burdensome networking and security processing workloads from the server CPU. Combined, this returns valuable and limited CPU resources to the applications and services that need them.
- **Hyperscale innovation:** Based on powerful, highly-programmable FPGAs, the solution guarantees hardware performance and the speed of software innovation, allowing network operators to both scale and evolve their standard computing platforms without compromise.

Jarrold J.S. Siket, CMO of Napatech, said:

"With 5G on the horizon, mobile network operators face the challenge of meeting the CPU-intensive demands of mobile applications on standard platforms. In response, Napatech has developed the Reconfigurable Accelerated RAN solution, which allows network operators to alleviate CPU bottlenecks and increase performance by as much as 40x."

ABOUT NAPATECH

Napatech helps companies to reimagine their business by bringing hyperscale computing benefits to IT organizations of every size. We enhance open and standard virtualized servers to boost innovation and release valuable computing resources that improve services and increase revenue. Our Reconfigurable Computing Platform™ is based on a broad set of FPGA software for leading IT compute, network and security applications that are supported on a wide array of FPGA hardware designs.

Additional information is available at www.napatech.com

NO FORWARD-LOOKING STATEMENTS

This press release may contain forward-looking statements which are only predictions and may differ materially from actual future events or results due to a variety of factors, including but not limited to, business conditions, trends in the industry and markets, global economic and geopolitical conditions, macro-economic factors, and other risks and uncertainties set forth in Napatech's reports. The matter discussed in this release is based on current expectations and may be subject to change. Napatech will not necessarily update this information.

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